## icana

#### ARQSF1828-RX-A

PRODUCT SUMMARY





### 1.8 – 2.8 GHz DUAL-CHANNEL RECEIVER FRONT-END MODULE

ARQSF1828-RX-A is a dual-channel, receiver front-end module (FEM) designed for 3GPP 5G NR FR1 bands n1, n2, n3, n7, n25, n30, n34, n38, n39, n40, n41, n53, and n65 Wireless Infrastructure. Each channel consists of a silicon SPDT switch and a two-stage GaAs LNA with bypass. The LNA provides high and low gain modes with 1.3 dB noise figure. The SPDT supports high power input signals up to 12 W average power with 10 dB PAR.

This FEM is part of iCana's compact pin-to-pin compatible FEM product line designed for Wireless Infrastructure supporting major 3GPP bands.



40 Pad  $6 \times 6 \text{ mm}^2 \text{ SMT Package}$ 

#### **Key Features**

#### **Transmit Operation:**

- Power handling: 41 dBm average power (LTE signal, 10 dB PAR)
- Low insertion loss: 0.6 dB
- Tx-Rx isolation: > 65 dB (ANT to RXout, LNA power down)
- Channel isolation: > 50 dB (TERM to TERM)

#### **Receive Operation:**

- Amplification with by-pass: High gain = 38 dB, Low gain = 18 dB
- Low current consumption: High gain = 110 mA, Low gain = 40 mA
- Low noise figure: 1.3 dB
- Excellent linearity: OIP3 = 33 dBm
- Positive logic control voltage: 1.8 V or 3.3 V
- Tx-Rx isolation: > 35 dB (ANT to TERM)
- Channel isolation: 40 dB (RXOUT to RXOUT)
- Package dimensions: 6 × 6 × 0.85 mm<sup>3</sup>

# RXOUT-A BP-B BP-A PD-AB SWCTRL-AB RXOUT-B VDD2-B VDD1-B TERM-B Functional Block Diagram

#### **Typical Applications**

- 3GPP 5G NR FR1 n1, n2, n3, n7, n25, n30, n34, n38, n39, n40, n41, n53, and n65
- 5G NR FR1 and 4G LTE massive MIMO
- Wireless infrastructure
- FDD- and TDD-based communication systems

#### **Ordering Guide**

Part Name	Description
ARQSF1828-RX-A	1.8 – 2.8 GHz Dual-Channel Receiver Front-End Module
ARQSF1828-RX-A-EVB	Evaluation Board for ARQSF1828-RX-A

#### **5G NR FR1 FEM Product Line**

Product Name	Description
ARQSF1828-RX-A	1.8 – 2.8 GHz
ARQSF2442-RX-A	2.4 – 4.2 GHz
ARQSF3753-RX-A	3.7 – 5.3 GHz

#### **REV 1.0**

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